

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.: 10/762,789 Confirmation No.: 6124
Applicant(s): Douglas G. Anderson
Filed: January 22, 2004
Art Unit: 2814
Examiner: Abdul Kalam
Title: WAFER, INTERMEDIATE WAFER ASSEMBLY AND ASSOCIATED
METHOD FOR FABRICATING A SILICON ON INSULATOR WAFER
HAVING AN IMPROVED EDGE PROFILE

Docket No.: 039035/267930
Customer No.: 00826

September 7, 2006

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT Under 37 C.F.R. § 1.121

Sir:

In response to the Official Action dated June 7, 2006, please amend the above-identified patent application as follows:

Amendments to the Claims are reflected in the listing of claims beginning on page 2 of this paper.

Remarks begin on page 6 of this paper.